

Amendments to the Claims

This listing of claims will replace all prior versions and listings of claims in the application.

Claim 1 (previously presented): A method of programming a memory comprising:

- (a) attempting to program a bit at a designated address for a predetermined time;
- (b) testing the bit to see if it has been programmed;
- (c) increasing the predetermined time by approximately an order of magnitude;
- (d) repeating steps (a)-(c) until the bit at the designated address is programmed;

and

- (e) repeating steps (a)-(d) by advancing the designated address until all bits in the memory are programmed.

Claim 2 (original): The method of claim 1, further including the step of testing selected bits of the memory, and defining the initial predetermined time based on the testing.

Claim 3 (currently amended): A method of programming a memory comprising:

- (a) attempting to program a bit for a predetermined program time;
- (b) testing the bit to see if the bit has been programmed;
- (c) repeating steps (a)-(b) with a program time that is increased by an order of magnitude until the bit is programmed; and
- (d) repeating steps (a)-(c) by advancing to a next address until all bits are programmed.

Claim 4 (currently amended): The method of claim [[1]] 3, further including the step of testing selected bits of the memory, and defining the initial predetermined program time based on the testing.